



Wireless Sensor and Actuator Networks-Based Reliable Data Acquisition Mechanism

Anil Sharma, Dankan Gowda V., A. Yasmine Begum, D. Nageswari, S. Lokesh

Source Title: Energy Systems Design for Low-Power Computing (/book/energy-systems-design-low-power/293050)

Copyright: © 2023

Pages: 27

DOI: 10.4018/978-1-6684-4974-5.ch010

OnDemand:
(Individual Chapters)

\$37.50

Available

[Current Special Offers](#)



Abstract

The objective of deploying a wireless sensor network is to collect data about the environment in which it will be utilised and then to transmit that data to a distant sink where it will be used to estimate or reconstruct the environment or event. In order for the centralised sink to be able to accurately reconstruct or estimate the event and take the necessary actions on time, the wireless sensor and actuator network must be able to guarantee delivery of a sufficient amount of the information gathered by the deployed sensor nodes in a time-bound and coherent manner. In addition to the above-mentioned fundamental problem, reliability also refers to the network's capacity to tolerate defects up to a certain point without compromising performance. This chapter introduces a brand-new, dependable data acquisition technique that makes use of wireless sensor and actuator networks.

Chapter Preview

Top

Introduction

A Wireless Sensor Network (WSN) is deployed with the purpose of acquiring information and then passing on the acquired information to a remote sink where the information can be used to estimate or re-construct the environment or event. This requires the WSN to have the ability to sense the parameter or event under consideration, in the region of deployment, and then ensure reliable delivery of the information to a centralized sink where the information sensed by it can be used to re-construct the events (Abroshan, S., & Moghaddam, M. H. Y. 2014). However, in context of the Wireless Sensor & Actuator Networks the Network Latency time assumes much greater importance since the action taken by the Actuator nodes has to be time coherent with the event sensed by the deployed nodes failing which the control loop will become un-stable and erratic.

This chapter presents a novel Reliable Data Acquisition methodology using Wireless Sensor & Actuator Networks which meets the criterion of reliability as mentioned above. The proposed methodology, ensures, regarding a sensed event is reported to the centralized sink / Actuators for acceptable estimation or re-construction of the event detected, within the time constraint fixed by the control action to be taken by the Actuator nodes.

The objective in this approach is for the Sensor Network to continue its operation with as high reliability as is feasible under the given fault condition in the network (Adelstein, 2005). The fault conditions could vary from temporary loss of communication between a set of nodes, to permanent loss of a node because of damage or end of battery life of the node. The focus of design of algorithm tends to be on ensuring detection of event and then delivery of sensed despite the fault condition. Some algorithms also focus on detection of fault condition itself and then managing it.

Majority of these solutions propose algorithms with the ability to dynamically find alternate network paths in case a given network becomes unusable because of a fault condition. Many algorithms present methodology where the data sensed is transferred on multiple paths simultaneously to begin with thus ensuring delivery of information at sink despite some path becoming useless because of node failure (Ali, S., Fakoorian, A. & Taheri, H. 2017).

Complete Chapter List

Search this Book:

[Reset](#)

Table of Contents

[View Full PDF \(/pdf.aspx?tid=319982&ptid=293050&ctid=15&t=Table of Contents&isxn=9781668449745\)](#)

Detailed Table of Contents

[View Full PDF \(/pdf.aspx?tid=319983&ptid=293050&ctid=15&t=Detailed Table of Contents&isxn=9781668449745\)](#)

Preface

Rathishchandra Ramachandra Gatti, Chandra Singh, Srividya P., Sandeep Bhat

[View Full PDF \(/pdf.aspx?tid=319984&ptid=293050&ctid=15&t=Preface&isxn=9781668449745\)](#)

Chapter 1

Low-Power Methodologies and Strategies in VLSI Circuits (/chapter/low-power-methodologies-and-strategies-in-vlsi-circuits/319986) (pages 1-16)

Preethi, Sapna R., Mohammed Mujeer Ulla

Download This Chapter

 **\$37.50**

[Add to Cart](#)

[Preview Chapter \(/viewtitlesample.aspx?id=319986&ptid=293050&t=Low Power Methodologies and Strategies in VLSI Circuits&isxn=9781668449745\)](#)

Chapter 2

SOI Technology in Designing Low-Power VLSI Circuits (/chapter/soi-technology-in-designing-low-power-vlsi-circuits/319987) (pages 17-28)

Srividya P.

Download This Chapter

 **\$37.50**

[Add to Cart](#)

[Preview Chapter \(/viewtitlesample.aspx?id=319987&ptid=293050&t=SOI Technology in Designing Low-Power VLSI Circuits&isxn=9781668449745\)](#)

Chapter 3

Low-Power High-Speed Eight-Bit Universal Shift Register Design Using Clock Gating Technique (/chapter/low-power-high-speed-eight-bit-universal-shift-register-design-using-clock-gating-technique/319988) (pages 29-43)

Preeti Sahu

Download This Chapter

 **\$37.50**

[Add to Cart](#)

[Preview Chapter \(/viewtitlesample.aspx?id=319988&ptid=293050&t=Low Power High-Speed Eight-Bit Universal Shift Register Design Using Clock Gating Technique&isxn=9781668449745\)](#)